



INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Docket Number (Optional)

FIS920030099US1

Application Number

10 / 604 , 213

Applicant(s)

PETER BERASI, ET AL.

Filing Date

7/01/03

Group Art Unit

1763

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

WZ		FIS919990286US1-SERIAL NO. 09/596,754 (has been abandoned) Technical Report:"ESSONNES TIN CAP PROCESS DEVELOPMENT SUMMARY" NOV.24, 1994, 95A001149 Technical Report:"TEMPORY CHIP ATTACH TECHNIQUE FOR BURN IN OF CMOS CHIPS WITH TIN CAP PADS",

EXAMINER

Hubert Culbert

DATE CONSIDERED

11 / 14 / 05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.